

Markus Leiter

Senior FPGA Engineer

TECHNICAL SKILLS

Main Technical Skills	FPGA (5 yr.), Embedded Systems, verification testing, System Architecture (4 yr.)
Platforms	FPGA (5 yr.)
Methodologies, Paradigms and Patterns	System Architecture (4 yr.)
QA, Test Automation, Security	verification testing
Other Technical Skills	Efinix, Intel Quartus, Lattice Diamond, Lattice ICE40, Lattice Radiant, SoC FPGA, Xilinx Vivado